



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/829,380 Confirmation No. 7082
Applicant : A. HAYASHI et al.
Filed : April 22, 2004
Title : A SEMICONDUCTOR INTEGRATED CIRCUIT
CONTROLLING OUTPUT IMPEDANCE AND SLEW RATE
TC/AU : 2816
Examiner : T.T. Lam
Docket No. : H-1139
Customer No.: 24956

Entered w/ RCE

dtd 8/8/06

DMC

8/16/06

MAIL STOP: RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY

Sir:

In response to the Office Action (Final Rejection) of April 11, 2006, and the advisory action please amend the above-identified application as follows.

Remarks begin on page 2 of this paper.

A Request for Continued Examination and the required fee accompany this paper.

A Petition for a One-Month Extension of Time and the required fee accompany this paper.